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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | | | Complete if Known | |
| | | | | Application Number | 09/706157 |
| | | | | Filing Date | 11/03/00 |
| | | | | First Named Inventor | Stephen Melvin Enrique Musoll Mario D. Nemirovsky Nandakumar Sampath Hector Urdaneta |
| | | | | Group Art Unit | 2183 |
| | | | | Examiner Name | Eric Coleman |
| Sheet | 1 | of | 3 | Attorney Docket Number | MIPS.0171-00-US |

| U.S. PATENT DOCUMENTS | | | | | | |
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| Examiner Initials | Cite No. ¹ | U.S. Patent Document | | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | Number | Kind Code ² | | | |
| EC | AA | 4916652 | | Schwarz et al | 04/10/1990 | |
| EC | AB | 5404469 | | Chung et al | 04/04/1995 | |
| EC | AC | 5574939 | | Keckler et al | 11/12/1996 | |
| EC | AD | 5742782 | | Ito et al | 04/21/1998 | |
| EC | AE | 5745725 | | Robert John Simpson | 04/28/1998 | |
| EC | AF | 5812811 | | Dubey et al | 09/22/1998 | |
| EC | AG | 5913049 | | Shiell et al | 06/15/1999 | |
| EC | AH | 5933627 | | Bodo Parady | 08/03/1999 | |
| EC | AI | 6092175 | | Levy et al | 07/18/2000 | |
| EC | AJ | 6105127 | | Kimura et al | 08/15/2000 | |


| FOREIGN PATENT DOCUMENTS | | | | | | |
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| | | Number ⁴ | Kind Code ⁵ | | | |
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| Examiner Signature | <i>Eric Coleman</i> | Date Considered | 10/28/05 |
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| Examiner Initials* | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| EC | BA | <i>The PowerPC Architecture: A Specification for a New Family of RISC Processors</i> , Second Edition, Morgan Kaufmann, San Francisco, pp.70-92 (May 1994). ✓ | |
| EC | BB | <i>MC68020 32-Bit Microprocessor User's Manual</i> , Third Edition, Prentice Hall, New Jersey, pp. 3-125, 3-126, and 3-127 (1989) ✓ | |
| EC | BC | Potel, M.J., "Real-Time Playback in Animation Systems," <i>Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques</i> , San Jose, CA, pp. 72-77 (1977) ✓ | |
| EC | BD | <i>ARM Architecture Reference Manual</i> , Prentice Hall, pp. 3-41, 3-42, 3-43, 3-67, 3-68 (1996). ✓ | |
| EC | BE | <i>ESA/390 Principles of Operation</i> , IBM Library Server, Table of Contents and Para.7.5.31 and 7.5.70 (1993) (available at: http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENTS) | |
| EC | BF | <i>MC88110 Second Generation RISC Microprocessor User's Manual</i> , Motorola, Inc., pp.10-66, 10-67, and 10-71 (1991). ✓ | |
| EC | BG | Diefendorff, Keith et al., "Organization of the Motorola 88110 Superscalar RISC Microprocessor," <i>IEEE Micro</i> , Vol, 12, No. 2, pp.40-63 (1992) ✓ | |
| EC | BH | Kane, Gerry, <i>PA-RISC 2.0 Architecture</i> , Prentice Hall, New Jersey, pp. 7-106 and 7-107 (1996) ✓ | |
| EC | BI | Diefendorff, Keith et al., "Altivec Extension to PowerPC Accelerates Media Processing," <i>IEEE Micro</i> , Vol, 20, No. 2, pp.85-95 (March-April 2000) ✓ | |
| EC | BJ | Diefendorff, Keith, "WinChip 4 Thumbs Nose at ILP," <i>Microprocessor Report</i> , Vol, 12, No.16, 10 pages (December 7, 1998) ✓ | |
| EC | BK | Slater, Michael, "Rise Joins x86 Fray with mP6," <i>Microprocessor Report</i> , Vol, 12, No. 15, 10 pages (November 16, 1998) ✓ | |
| EC | BL | Diefendorff, Keith, "Jalapeno Powers Cyrix's M3," <i>Microprocessor Report</i> , Vol. 12, No. 15, 11 pages (November 16, 1998) ✓ | |
| EC | BM | Becker, Michael C., et al, "The PowerPC 601 Microprocessor," <i>IEEE Micro</i> , Vol, 13, No.5, pp.54-68 (October 1993) ✓ | |

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| Examine Initials | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | T ² |
| EC | BN | Diefendorff, Keith, "Compaq Chooses SMT for Alpha," <i>Microprocessor Report</i> , Vol, 13, No. 16, 13 pages (December 6, 1999) ✓ | | | |
| EC | BO | Eggers, Susan J., et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," <i>IEEE Micro</i> , Vol, 17, No. 5, pp. 12-19 (September/October 1997) ✓ | | | |
| EC | BP | Gwennap, Linley, "Digital 21264 Sets New Standard," <i>Microprocessor Report</i> , Vol, 10, No. 14, 11 pages (October 28, 1999) ✓ | | | |
| EC | BQ | Diefendorff, Keith, "Power4 Focuses on Memory Bandwidth," <i>Microprocessor Report</i> , Vol, 13, No. 13, 13 pages (October 6, 1999) ✓ | | | |
| EC | BR | Diefendorff, Keith, "K7 Challenges Intel," <i>Microprocessor Report</i> , Vol, 12, No. 14, 7 pages (October 26, 1998) ✓ | | | |
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